

## STUDY AND EVALUATION SCHEME FOR DIPLOMA PROGRAMME IN

### FIFTH SEMESTER (ELECTRONICS ENGINEERING)

Sr. No	Subject	L	T	P	T O T	EVALUATION SCHEME						Total Marks
						Internal Assessment		External Assessment (Examination)				
		Periods/Week				Theory	Practical	Theory		Practical		
		Max. Marks	Max. Marks	Max. Marks	Hrs	Max. Marks	Hrs					
	Industrial Training	4 Weeks				-	25	-	-	25	3	50
5.1	Consumer Electronics and Troubleshooting	3	1	4	8	25	25	50	2.5	50	3	150
5.2	Microwave Communication	4	-	2	6	25	25	50	2.5	50	3	150
5.3	Advanced Microprocessor	4	-	4	8	25	25	50	2.5	50	3	150
5.4	Electronics in Industry	4	-	4	8	25	25	50	2.5	50	3	150
5.5	Digital Switching and Circuits	4	-	4	8	25	25	50	2.5	50	3	150
5.6	Process Control and Instrumentation	4	-	2	6	25	25	50	2.5	50	3	150
General Proficiency #		-	-	4	4	-	25	-	-	-	-	25
Industrial Exposure (Assessment at Inst. Level) +		-	-	-	-	-	25	-	-	-	-	25
<b>Total</b>		<b>23</b>	<b>1</b>	<b>24</b>	<b>48</b>	<b>150</b>	<b>225</b>	<b>300</b>	<b>-</b>	<b>325</b>	<b>-</b>	<b>1000</b>

# General Proficiency will comprise of various co-curricular activities like games, hobby clubs, seminars, declamation contests, extension lectures, NCC, NSS and cultural activities etc.

+ Industrial visit compulsory to minimum 2 Industries or Departments.

- Note:**
- 1- Each period will be 50 minutes.
  - 2- Each session will be of 16 weeks.
  - 3- Effective teaching will be at least 12.5 weeks.

